

**Notice of Allowability**

Application No.

09/698,622

Examiner

Emmanuel Bayard

Applicant(s)

JONG ET AL.

Art Unit

2611

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--**

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to communication filed on 4/5/06.
2. ☒ The allowed claim(s) is/are 1-7 and 9-14.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

**Attachment(s)**

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_.

### **DETAILED ACTION**

This is in response to amendment filed on 4/5/06 in which claims 1-7 and 9-14 are pending. The applicant's amendments have been fully considered therefore this case is in condition for allowance.

#### ***Allowable Subject Matter***

1. The following is an examiner's statement of reasons for allowance: the prior arts of record fail to anticipate or render obvious the following recited features: a circuit that processes the high comparator output and the low comparator output, wherein both the high comparator output and the low comparator output directly clock the circuit, wherein at least one of the output from the high comparator and the output from the low comparator is operatively connected to a clock input of a logic device, and wherein the circuit generates an alarm if a noise error is detected as recited in claim 1. A low-to-high sub-circuit that detects a noise error during a falling signal transition, wherein the low-to-high sub-circuit comprises, a plurality of flip-flop circuits clocked by both the output of the high comparator and the output of the low comparator; a delay buffer; and an XOR logic gate; and wherein at least one of the high-to-low sub-circuit and the low-to-high sub-circuit generates an alarm if a noise error is detected as recited in claim 7.

Activating an alarm if the low signal voltage is greater than the low voltage limit, wherein the activating is dependent on being clocked by the second signal, wherein at least one of the first signal and the second signal is operatively connected to a clock input of a logic device as recited in claim 9. Comparing a low signal voltage with a low voltage limit using a low-to-high sub-circuit that detects the noise error during a falling signal

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transition, wherein the low-to- high sub-circuit comprises, a plurality of flip-flop circuits clocked by a signal generated dependent on the comparing, a delay buffer, and an XOR logic gate; and activating an alarm if the low signal voltage is greater than the low voltage limit as recited in claim 13.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Emmanuel Bayard whose telephone number is 571 272 3016. The examiner can normally be reached on Monday-Friday (7:Am-4:30PM) Alternate Friday off.

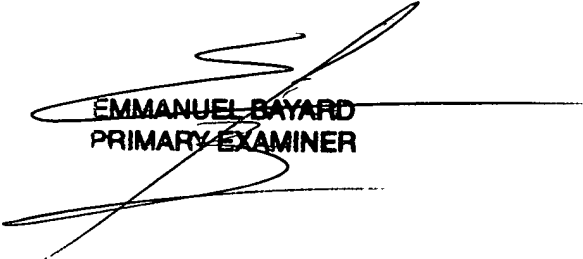
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay Patel can be reached on 571 272 2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Emmanuel Bayard  
Primary Examiner  
Art Unit 2611

6/23/06

  
EMMANUEL BAYARD  
PRIMARY EXAMINER